

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Volkan Kursun et al.

SERIAL NO: not yet assigned

GAU: Not yet assigned

FILED: herewith

EXAMINER: Not yet assigned

FOR: DOMINO LOGIC WITH VARIABLE THRESHOLD VOLTAGE KEEPER

## INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS  
 P.O. BOX 1450  
 ALEXANDRIA, VA. 22313-1450

SIR:

Applicant(s) wish to disclose the following information.

## REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- A check is attached in the amount required under 37 CFR §1.17(p).

## RELATED CASES

- Attached is a copy of applicant's pending application(s) or issued patent(s) which may be related to the present application. These documents are listed on form PTO-1449, also attached.
- A check is attached in the amount required under 37 CFR §1.17(p).

## CERTIFICATION

- Each item of information contained in this information disclosure statement was cited for the first time in any communication from a foreign patent office in any counterpart foreign application not more than three months prior to the filing of this statement.
- No item of information contained in this information disclosure statement was cited for the first time in any communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.
- This Information Disclosure Statement is being filed within three months of the filing date of the subject patent application.
- This Information Disclosure Statement is being filed before the mailing date of a first Office Action on the merits.

## PETITION

- Applicant(s) hereby request consideration of the attached information. A check is attached in the amount of the Petition fee required under 37 CFR §1.17(i)(1).

## DEPOSIT ACCOUNT

- Please charge any additional fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit account number 23-2185. A duplicate copy of this sheet is enclosed.

Respectfully Submitted,

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27557

PATENT TRADEMARK OFFICE

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Date: June 30, 2003

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 000687-00302	SERIAL NO. Not yet assigned		
<b>LIST OF REFERENCES CITED BY APPLICANT</b>		APPLICANT Volkan Kursun et al					
		FILING DATE herewith	GROUP Not yet assigned				
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER INITIAL	DOC. NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE	
AA							
AB							
AC							
AD							
AE							
AF							
AG							
AH							
AI							
AJ							
AK							
AL							
AM							
AN							
<b>FOREIGN PATENT DOCUMENTS</b>							
	DOC. NUMBER	DATE	COUNTRY	TRANSLATION YES NO			
AO				<input type="checkbox"/> <input type="checkbox"/>			
AP				<input type="checkbox"/> <input type="checkbox"/>			
AQ				<input type="checkbox"/> <input type="checkbox"/>			
AR				<input type="checkbox"/> <input type="checkbox"/>			
AS				<input type="checkbox"/> <input type="checkbox"/>			
AT				<input type="checkbox"/> <input type="checkbox"/>			
AU				<input type="checkbox"/> <input type="checkbox"/>			
AV				<input type="checkbox"/> <input type="checkbox"/>			
<b>OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
AW	V. Kursun and E. G. Friedman, "Domino Logic with Variable Threshold Voltage Keeper," IEEE Transactions on Very Large Scale Integration (VLSI) Systems (In press).						
AX	V. Kursun and E. G. Friedman, "Variable Threshold Voltage Keeper for Contention Reduction in Dynamic Circuits," Proceedings of the IEEE International ASIC/SOC Conference, pp. 314-318, September 2002						
AY	V. Kursun and E. G. Friedman, "Domino Logic with Dynamic Body Biased Keeper," Proceedings of the European Solid-State Circuits Conference, pp. 675-678, September 2002.						
AZ							
Examiner	Date Considered						

\*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.